IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Fred D. Bailey et al.

Art Unit: 2815

Serial No.:

09/452,691

Examiner:

J. A. Fenty

Filing Date:

12/02/1999

Docket No.:

TI-27935 1638

Customer No.: 23494

Conf. No.: 1638

Title: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED WITHIN A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER METAL INTERCONNECT LAYER

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents PO Box 1450 Alexandria, VA 22313-1450

or Sic

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by factimite to the U.S. Patent and Trademark Office at 703-872-9308 on

the date shown below

lacquethe J. Gamer, Reg. No. 36,144

July 7, 2004

Date

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

One Month (\$110)

☐ Two Months (\$420)

Three Months (\$950)

☐ Four Months (\$1,480)

Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348 Fax: (972) 917-4418 Respectfully Submitted,

Jacqueline J. Gamer Reg. No. 36,144

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PAGE 4/16 * RCVD AT 7/7/2004 4:56:44 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/7 * DNIS:8729306 * CSID:972 278 9775 * DURATION (mm-ss):05-34